

## FACULTY PROFILE

<b>Name:</b> PRAMOD P.	<b>Address:</b> UPASANA, NEAR EMS MANDIRAM, KOVVAL STORE,  KANHANGAD SOUTH P.O.,  KASARAGOD DISTRICT
<b>Designation:</b>	<b>Email:</b> pramodp2006@gmail.com
<b>Department:</b> ECE	<b>Mobile:</b> 9447855635
<b>ACADEMIC QUALIFICATIONS</b>	
Doctoral ( <i>Institute, year, Subject</i> )	Pursuing Ph.D( Division of Electronics, School of Engineering, Cochin University of Science and Technology (CUSAT), Kerala, India.)
P.G.	M.E.VLSI DESIGN, ANNA UNIVERSITY, 2012
U.G.	B.E. ECE, MADRAS UNIVERSITY, 2000
Other qualifications	MBA- ICFAI University.  Executive Program for Young Professionals (post graduate management program) - IIM Calcutta
<b>AREA OF INTEREST</b>	
<b>Digital System Design, Low Power Techniques, VLSI Design &amp; Testing, and VLSI Signal Processing.</b>	
<b>WORK EXPERIENCE</b>	
Teaching ( <i>Period, position, Organization</i> )	Assistant Professor, ECE Department, LBS College of Engineering, Kasaragod (11/09/2002 to till date)

Industry	---
Others	---
<b>RECENTLY TAUGHT COURSES</b>	
VLSI DESIGN, LOW POWER VLSI, CONTROL SYSTEMS, VLSI SIGNAL PROCESSING, CMOS VLSI, DIGITAL SYSTEM DESIGN	
<b>OTHER RESPONSIBILITIES</b>	
<b>PUBLICATIONS (LATEST)</b>	
Peer Reviewed Science cited International Journals	<p>1)[1] Pramod P. and Shahana T.K., "An Efficient Architecture for Signed Carry Save Multiplication," <i>IEEE Letters of the Computer Society</i>, vol. 3, no. 1, pp. 9-12, 1 Jan.-June 2020.</p> <p>2)Pramod P. and Shahana T.K., "Efficient modular hybrid adders and Radix-4 booth multipliers for DSP applications", <i>Microelectronics Journal</i>, Elsevier, January 2020, Print ISSN 0026-2692</p> <p>3)Pramod P., and Shahana T. K., "High throughput FIR filter architectures using retiming and modified CSLA based adders," <i>IET Circuits, Devices and Systems</i>, Vol.13, No.7, pp.1007-17, 2019.</p> <p>4)Pramod, P., Shahana, T.K., "High throughput adaptive filter architecture using modified transpose form FIR filters", <i>Journal of Advanced Research in Dynamical and Control Systems</i>, vol.10, no.15, pp. 68-82, Nov. 2018</p>
Peer Reviewed Science cited International Conferences	<p>1)Pramod P. and Shahana T.K., "High throughput and energy efficient FIR filter architectures using retiming and two level pipelining", <i>Third International Conference on Computing and Networking Communications (COCONET)</i>, Thiruvananthapuram, 18-20, December 2019</p>

Date: 17 03 20

Signature: