

HOW TO APPLY

Apply in the following URL

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LAST DATE TO APPLY:

30th April 2026

Whatzapp Link

https://chat.whatsapp.com/BAGM9gB71fPACHe4ffq9Pw?mode=gi_t

REGISTRATION FEE

IEEE Members: ₹ 150/-

Non-IEEE Members: ₹ 300/-

ACCOUNT DETAILS:

Account Name: ECE Association

Account Number: 016922010002167

IFSC Code: UBIN0901695

Branch Name: Union Bank of India, Bovikanam

SCAN FOR PAYMENT



OBJECTIVES

The objective of this FDP is to bridge the gap between traditional engineering practices and the growing need for eco-friendly, energy-efficient technological solutions. The programme aims to provide in-depth knowledge on sustainable manufacturing, green technology, and eco-friendly design practices in electronics. Also, this FDP seeks to educate the faculty members on the integration of AI, ML, and IoT in renewable energy systems, smart grids, and energy harvesting, which are critical for sustainable electronics.

Topics Covered

- Design and modeling of power FETs for sustainable smart mobility applications.
- Edge intelligence for sustainable industry 4.0: Reducing Energy, Latency and E-Waste.
- Tiny ML
- Sustainability in VLSI Design.
- Shaping a Greener Future: The usage of AI and Quantum computing to provide energy sustainability and Climate Resilience.
- Machine learning for sustainable Hardware security.

Resource Persons

- Dr. Nikhil K S, NIT Karnataka
- Dr. Lizy Abraham, South East Technological University, Ireland
- Prem Krishnan N, Scientist, CDAC
- Dr. Rahul, Amrita Vishwa Vidyapeetham, Coimbatore.
- Dr. Krishnapriya S, Christ College of Engineering, Irinjalakuda
- Aksa David, VLSI Trainer, IBIS Group of Institutions



IEEE Kerala Section



L.B.S. COLLEGE OF ENGINEERING KASARAGOD
(A GOVT. OF KERALA UNDERTAKING)

DEPARTMENT OF ELECTRONICS & COMMUNICATION

in association with
IEEE kerala Section

6 DAY ONLINE FDP ON:

**Sustainability
in
Electronics**

4th - 9th May 2026

Co-ordinated by

Dr. Anusree L - 9446177965

Dr. Baiju P S - 9895016065