

LBS COLLEGE OF ENGINEERING ,KASARAGOD

QUOTATION NOTICE

No.D-1449/2024-2

Date: 17.01.2026

Sealed competitive Quotations are invited by the Principal, LBS College of Engineering, Kasaragod from authorised dealers for the supply of Equipment to Microprocessors and Embedded Systems Lab of EEE Department of this Institution.

Quotation should be super scribed "Quotation for the supply of equipment to the Microprocessor and Embedded Systems Lab against Quotation No D-1449/2024-2 due on 27-01-2026. The rates quoted should be for the Supply of the items at LBS College of Engineering, Povval, Muliya PO, Kasaragod. Quotations received after the time fixed on the due date is liable to be rejected. Maximum period required for the delivery of the items should also be mentioned.

The prices quoted should be inclusive of all taxes. The quotationer shall also quote the percentage of rebate/discount if any offered by them against the items. Special conditions if any or printed on quotation will not be applicable to the contract unless they are explicitly accepted in writing by the undersigned.

Due date and time for receipt of quotation : 27-01-2026 2 PM

Date and time of opening the quotation : 27-01-2026 3 PM

Date up to which the rates are to remain firm
for acceptance : 6 Months

To whom the quotation is to be addressed : The Principal,
LBS College of Engineering
Muliya (P O),
Kasaragod-671542.

Encl: Specification of item

Copy To:

- 1 College Website
- 2 HOD, EEE
- 3 Notice Board




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SPECIFICATION OF ITEMS

Sl No	Item	Specification	Qty
1	8051 CONTROLLER TRAINER KIT WITH USB cable for Communication between kit to PC	<p>The equipment shall be based on an 8051 / AT89C51 / 89x51 microcontroller with a clock speed of 12 MHz. The system shall provide 32 KB Program Memory (RAM), 32 KB Data Memory (RAM), and 32 KB Program Memory (EPROM). The trainer shall be provided with a 16 × 2 LCD display, a 105-key PC keyboard, and reset and interrupt (RRE) keys.</p> <p>The unit shall support parallel I/O bus expansion through one 8255 PPI interface terminated on a 26-pin FRC header providing 24 I/O lines, one VXT bus (50-pin FRC connector) for interfacing VBMB series experiment boards and bus expansion, and a 40-pin FRC header for microcontroller port lines.</p> <p>The trainer shall include 8 on-board digital inputs through an 8-way DIP switch and 8 on-board digital outputs indicated using SMD LEDs. Port P1 shall be configurable as either input or output through hardware selection. Software single-stepping of user programs at instruction level shall be provided for debugging, during which user interrupts shall be disabled.</p> <p>The system shall include an on-chip RS-232 compatible serial interface terminated in a 9-pin D-type male connector, along with a USB-to-Serial converter. The unit shall operate from a built-in regulated power supply with 230 V AC input and outputs of +5 V / 3 A and +12 V / 0.5 A, and shall include a 3 V lithium battery backup.</p> <p>The equipment shall be housed in a durable plastic cabinet suitable for laboratory use. Required documentation, including user manual and experimental procedures, shall be provided. The equipment shall carry a minimum warranty of one (1) year against manufacturing defects.</p>	4
2	Arduino UNO R3 SMD with Cable		2




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