

HOW TO APPLY

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LAST DATE TO APPLY:

30th November 2025

Registration Fee

IEEE Members : ₹ 150/-

Non-IEEE Members: ₹ 250/-

Account Details:

Account Name: ECE Department Library LBSCE

Account Number: 3213182657

IFSC Code: CBIN0283191

Branch Name: Central Bank of India, Kasargod.

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WHO CAN ATTEND

The FDP is open to Faculty members, research scholars, and postgraduate students from government, aided, and self-financing engineering as well as polytechnic institutions across the country who are interested in exploring the applications of Artificial Intelligence in VLSI design and related emerging technologies.

RESOURCE PERSONS

Dr. Jayaraj U. Kidav,
Executive Director at NIELIT,
Aurangabad

Dr. Jose Joseph
Chair, School of Electronic Systems and Automation
Kerala University of Digital Sciences, Innovation and Technology

Dr. Sajesh Kumar U
Professor, Department of ECE,
GCEK, Kannur

Dr. Sakthivel R
Associate Professor, School of Electronics Engineering (SENSE)
Vellore Institute of Technology, VIT University

Dr. Premjith B
Assistant Professor (Sr. Grade),
Amrita School of Artificial Intelligence,
Amrita Vishwa Vidyapeetham, Coimbatore

Sreejeesh S G
Senior Technical Officer
NIELIT, Calicut

CERTIFICATION

The FDP sessions will be conducted **online** through a suitable virtual platform. Sessions will be held in the **evening from 7:00 p.m. to 8:30 p.m.** to ensure convenient participation for faculty members.

Participants are required to maintain a **minimum of 80% attendance** throughout the programme to be awarded the **certificate of participation**.

OBJECTIVES

The objective of this Faculty Development Programme is to familiarize participants with the emerging convergence of Artificial Intelligence (AI) and Very Large-Scale Integration (VLSI) technologies. The programme aims to provide an understanding of how AI-driven techniques can be leveraged to enhance various stages of VLSI design and optimization, including circuit modelling, layout automation, fault detection, and performance prediction. By exploring current research trends, design methodologies, and practical applications, this FDP seeks to equip faculty members with the knowledge to integrate AI concepts into VLSI teaching and research.

TOPICS COVERED

- Emerging VLSI technologies: 3D ICs, FinFETs, and nanoelectronics
- Synergy of AI and VLSI: How AI is shaping chip design, embedded systems, and edge computing
- Pedagogy, Research & Future directions in the AI era
- Deep Learning: CNNs, RNNs, and Transformer models
- Neuromorphic Computing and AI-inspired hardware design, AI accelerators: Concepts (TPU, NPU, FPGA-based AI)
- AI in VLSI education and research: case studies and success stories.

ABOUT THE INSTITUTION

L.B.S. College of Engineering, Kasaragod (LBSCEK), established in 1993 under the aegis of the L.B.S. Centre for Science & Technology, is located on a sprawling 52-acre campus in Povval, Muliya (PO), Kasaragod. As the only government engineering college in the district, it offers six full-time B.Tech programmes and a PG programme, all affiliated to APJ Abdul Kalam Technological University. With a rich history of academic excellence, research initiatives, industry collaboration, and a dedicated placement cell, LBSCEK is committed to empowering students from the region with quality technical education and fostering sustainable growth. The college currently offers B.Tech programmes in Computer Science and Engineering, Computer Science & Business Systems, Artificial Intelligence & Data Science, Electronics and Communication Engineering, Electrical and Electronics Engineering, Mechanical Engineering, Civil Engineering, and Information Technology.

ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering at L.B.S. College of Engineering, Kasaragod, has been a cornerstone of the institution since its inception in 1993, offering a dynamic blend of theoretical foundation and practical exposure in core and emerging technologies. The department is committed to imparting high-quality education in areas such as VLSI design, embedded systems, signal processing, communication networks, and artificial intelligence applications in electronics. Supported by well-equipped laboratories, experienced faculty, and active student associations, the department fosters innovation, research, and professional development. Through its emphasis on hands-on learning, interdisciplinary projects, and industry collaboration, the ECE department prepares graduates to excel in academia, research, and diverse engineering industries.

IEEE Kerala Section



Lal Bahadur Shastri College of Engineering, Kasaragod
(A Govt. of Kerala Undertaking)

Department of Electronics & Communication
&
Research & Development Cell, LBSCEK

in association with
IEEE Kerala Section

6 DAY ONLINE FDP ON:

**Exploring the Opportunities
of AI in VLSI systems**

2nd – 7th December 2025

ORGANIZING COMMITTEE

DIRECTOR, LBS CENTRE FOR SCIENCE & TECHNOLOGY
Dr. M. Abdul Rahiman

PRINCIPAL
Dr. Mohammad Sekoor T

HEAD OF DEPARTMENT
Dr. Mary Reena K E

ORGANIZERS
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